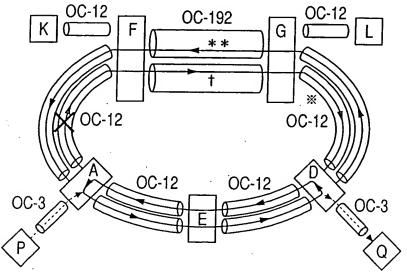


* : K1 Tr#i BYTE=K1 BYTE FROM APPARATUS A K2 Tr#i BYTE=K2 BYTE FROM APPARATUS A

* * : K1 Tr#i BYTE=K1 BYTE FROM APPARATUS D K2 Tr#i BYTE=K2 BYTE FROM APPARATUS D

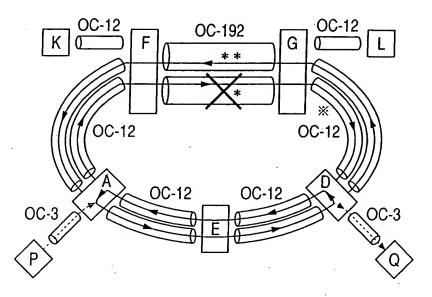
FIG. 2



* * : K1 Tr#i BYTE=K1 BYTE FROM APPARATUS D K2 Tr#i BYTE=K2 BYTE FROM APPARATUS D

† : K1 Tr#i BYTE=K2 Tr#i BYTE=(FF)hex

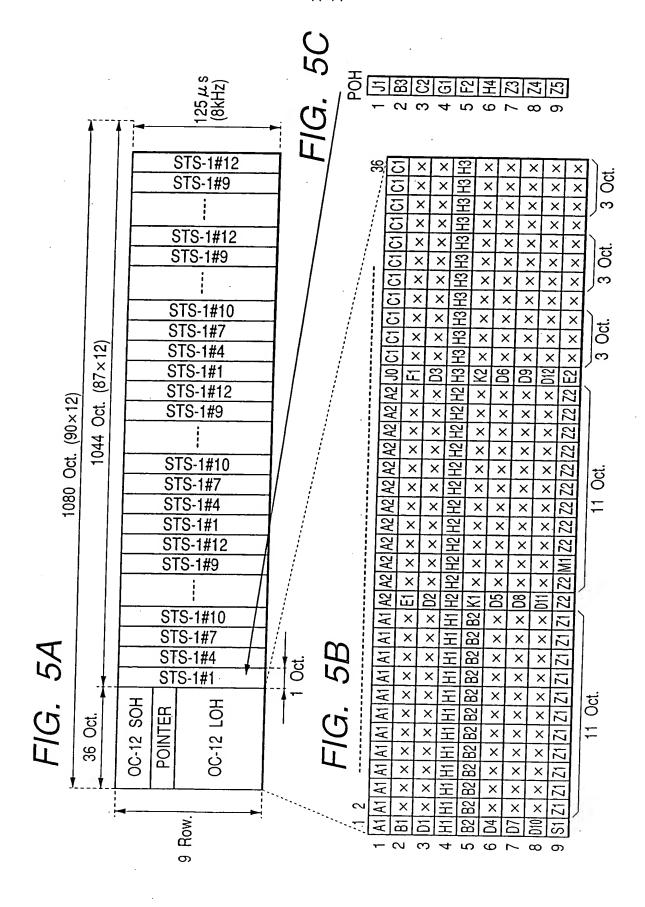
* : K1 BYTE=K2 BYTE=(FF)hex

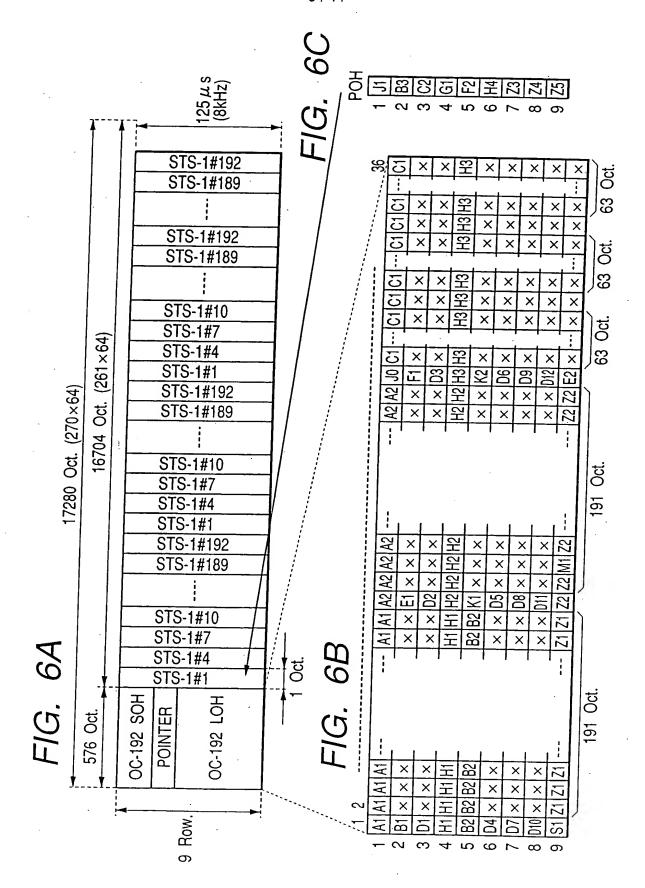


- * : K1 Tr#i BYTE=K1 BYTE FROM APPARATUS A K2 Tr#i BYTE=K2 BYTE FROM APPARATUS A
- * * : K1 Tr#i BYTE=K1 BYTE FROM APPARATUS D
 - K2 Tr#i BYTE=K2 BYTE FROM APPARATUS D
 - * : K1 BYTE=K2 BYTE=(FF)hex

F1G. 4

OVERHEAD	1EAD	MOILONIE
	A1, 2A	FRAME SYNCHRONIZATION
SECTION	B1	ADMINISTRATION OF ERRORS IN SECTION INTERVAL
OVERHEAD	D1~D3	USED FOR MAINTENANCE OPERATION
	Ш	SPEECH COMMUNICATION FOR CRAFT MAN
	J0 (C1)	DESIGNATION OF OC-1 MULTIPLEX NIMBER OF OC M SIGNAL
	FI	USED FOR CONVENIENCE OF CRAFT MANI
	H1, H2	INDICATION OF HEAD PHASE OF PATH
LINE	H3	USED FOR FREQUENCY SYNCHRONIZATION
OVERHEAD	B2, M1	ADMINISTRATION OF ERRORS IN TIME INTERVAL
	K1, K2	SWITCHING CONTROL FOR LINE INTERVAL
	D4~D12	D4~D12 USED FOR MAINTENANCE OPERATION
	E2	SPEECH COMMUNICATION FOR CRAFT MAN
	S1	USED FOR OPERATION OF NETWORK SYNCHDONIES TION
	21, 22	INTERNATIONALLY RESERVED AS SEADE
		STATE AS STATE



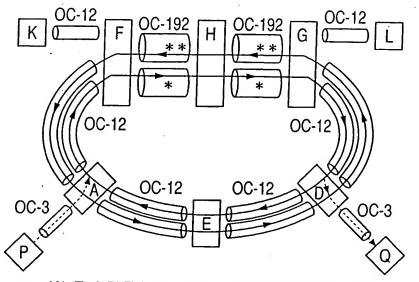


	87×192 BYTE				PAYLOAD	PAYLOAD	PAYLOAD	PAYLOAD	PAYLOAD	PAYLOAD	PAYLOAD	PAVIOAN	ייייייייייייייייייייייייייייייייייייייי	THROUGH BYTES			(13) ··· (61) (62) (63) (64) (BYTE)	A1 A1 A1 A1 A1	:		71		_		T#3 [0]		1 1 1	71
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	- 1	2	:	:	-	:	-	:	:	:	:	:	-]		(0)		A1 A1			포		_		1 1 1 1 1 1		7. 7.	7
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F/G. 10

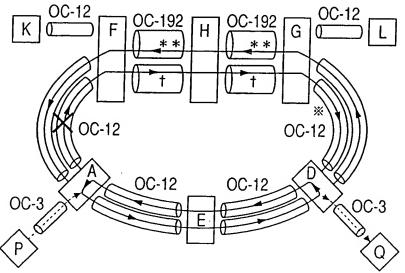
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* : K1 Tr#i BYTE=K1 BYTE FROM APPARATUS A K2 Tr#i BYTE=K2 BYTE FROM APPARATUS A

**: K1 Tr#i BYTE=K1 BYTE FROM APPARATUS D K2 Tr#i BYTE=K2 BYTE FROM APPARATUS D

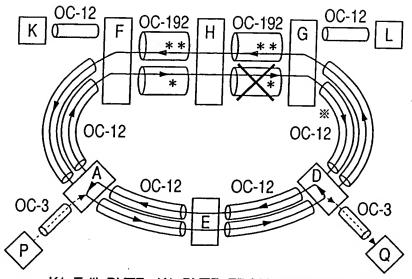
FIG. 12



* * : K1 Tr#i BYTE=K1 BYTE FROM APPARATUS D K2 Tr#i BYTE=K2 BYTE FROM APPARATUS D

† : K1 Tr#i BYTE=K2 Tr#i BYTE=(FF)hex

* : K1 BYTE=K2 BYTE=(FF)hex

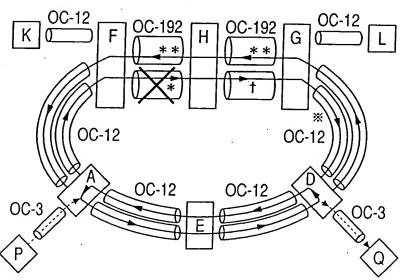


* : K1 Tr#i BYTE=K1 BYTE FROM APPARATUS A K2 Tr#i BYTE=K2 BYTE FROM APPARATUS A

**: K1 Tr#i BYTE=K1 BYTE FROM APPARATUS D K2 Tr#i BYTE=K2 BYTE FROM APPARATUS D

*: K1 BYTE=K2 BYTE=(FF)hex

FIG. 14



* : K1 Tr#i BYTE=K1 BYTE FROM APPARATUS A K2 Tr#i BYTE=K2 BYTE FROM APPARATUS A

**: K1 Tr#i BYTE=K1 BYTE FROM APPARATUS D
K2 Tr#i BYTE=K2 BYTE FROM APPARATUS D

† : K1 Tr#i BYTE=K2 Tr#i BYTE=(FF)hex

* : K1 BYTE=K2 BYTE=(FF)hex

FIG. 15

